

EUROPEAN PATENT OFFICE

57127220

Patent Abstracts of Japan



PUBLICATION NUMBER : 57127220
PUBLICATION DATE : 07-08-82

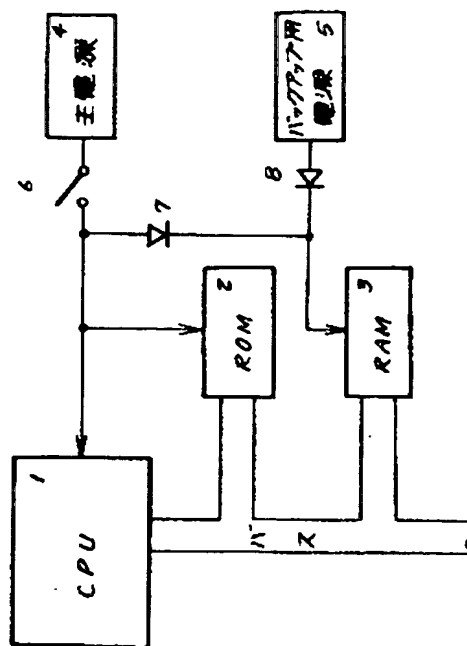
APPLICATION DATE : 29-01-81
APPLICATION NUMBER : 56012044

APPLICANT : FUJITSU LTD;

INVENTOR : SHIBAYAMA HITOSHI;

INT.CL. : G06F 1/00 G11C 29/00

TITLE : SYSTEM FOR DETECTING
TURNING-OFF OF BACKUP POWER
SOURCE FOR MEMORY



ABSTRACT : PURPOSE: To detect power-off of a power source for backup when a main power source is turned on again, by writing a specific bit pattern in a nonvolatile memory to store information before power-off of the main power source.

CONSTITUTION: When a switch 6 is turned on, a CPU1 is started to compare a specific bit pattern stored in an ROM 2 with contents in a determined address of an RAM3. If they do not coincide with each other, all contents of the RAM3 are made ineffective, and the specific bit pattern stored in the ROM2 is written in a determined address of the RAM3. If they coincide with each other, all contents of the RAM3 are decided effective. If a power source 5 for backup is connected normally when the switch 6 is turned off, contents of the RAM3 are held because power is supplied continuously through a diode 8.

COPYRIGHT: (C)1982,JPO&Japio